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FOR IMMEDIATE RELEASE

**Ian Barton Joins Silistix as Managing Director of
Silistix UK Ltd.**

- *Industry veteran brings valuable complex SoC design-team management experience to self-timed interconnect leader*

San Jose, Calif. – December 18, 2007 – Silistix, a leading provider of innovative software for the design of on-chip communications solutions, announced that it has appointed Ian Barton as the company’s Managing Director for its Silistix UK, Ltd. Subsidiary. Barton will manage the operations of Silistix’s development facility in Manchester, UK.

Prior to accepting the appointment as Managing Director at Silistix, Ian managed design facilities for complex devices across multiple sites at Zetex Semiconductors and Motorola Ltd. He has directed all aspects of designing devices and related software for a broad range of products including new FPGA architectures, DSPs, and ASICs targeted at key end markets including networking and mobile communications.

“Silistix’s self-timed interconnect solutions solve a set of obvious chip design problems,” notes Ian Barton. “My experience leading a multi-site team designing very large system-on-chip solutions for mobile base-station applications proved that achieving final timing closure with traditional clock-based synchronous interconnect could literally take months, even with state-of-the-art timing-driven tools. The Silistix solution eliminates that time-consuming and expensive headache.”

“We are extremely pleased to have Ian join the Silistix team,” said David Fritz, Silistix CEO. “His broad background in managing geographically diverse design teams will be critical as we expand our engineering capabilities worldwide to support increasing

acceptance of Silistix products and self-timed design. Ian's experience will also be an important factor in providing superlative support to our growing customer base."

About Silistix

Silistix is the leading supplier of on-chip interconnect solutions delivering predictable power, performance and area while cutting overall chip design time and effort. Silistix EDA tools and advanced circuit IP enable design teams to overcome fundamental challenges including global timing closure, clock distribution, power management, and utilization of the latest process technologies while meeting the extreme market pressures of converged consumer electronics products. The company is venture funded and has offices in Manchester, England, San Jose, California, and Tokyo, Japan. For more information on Silistix and its products visit www.silistix.com.

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