



FOR IMMEDIATE RELEASE

*** MEDIA ALERT ***

Join Silistix at DAC 2009 Panel:

Embedded Multicore - Multi-Opportunities, Multi-Challenges

What: Silistix Inc. Senior Architect, Robert Adair, will take part in a panel entitled “Embedded Multicore: Multi-Opportunities, Multi-Challenges” at the Design Automation Conference in the Moscone Center. Multicore processors are pervasive throughout desktop applications, regardless of their inherent programmability challenges. Now these processors are emerging in high-performance embedded SOCs, an environment with far greater design constraints, driving optimal programming requirements. This panel addresses the opportunity for success for these advanced devices. Are they the catalyst for change between HW and SW team processes? Joining Mr. Adair on the panel will be industry experts from ARM, Target Compiler Technologies, and Tensilica.

Silistix staff will be available throughout the conference to answer questions about the panel and their award winning CHAIN®works family of Network-on-Chip silicon IP and tools.

To schedule a meeting with Silistix about their Network-on-Chip offerings while in town for DAC email: dac09@silistix.com

When: July 28th 2009 (Tuesday)
Panel: 1:00 PM PDT

Where: Design Automation Conference (DAC) 2009
The Moscone Center
747 Howard St, San Francisco, CA
Booth #1928
www.dac.com

About Silistix

Silistix, Inc. is a leading Silicon Intellectual Property (SIP) vendor, delivering predictable Network-on-Chip (NoC) solutions for managing chip complexity. Silistix CHAIN®works family of tools and IP libraries address timing closure, power management, and deep-sub-micron process variability while cutting chip design time and effort. Silistix is privately held and is backed by a number of venture firms and corporate investors, including Intel Capital. The company has offices in Manchester, England; San Jose, California; and Tokyo, Japan. For more information, visit www.silistix.com.

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